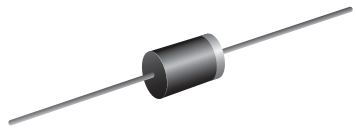
SR5150 - SR5200

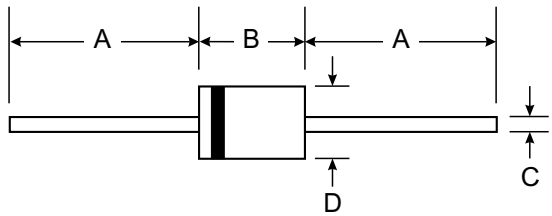
SCHOTTKUY BARRIER RECTIFIER DIODES

VOLTAGE RANGE: 150 - 200V
CURRENT: 5.0 A



Features

- Schottky Barrier Chip
- Ideally Suited for Automatic Assembly
- Low Power Loss, High Efficiency
- For Use in Low Voltage Application
- Guard Ring Die Construction
- Plastic Case Material has UL Flammability Classification Rating 94V-O



DO-201AD		
Dim	**Min**	**Max**
A	25.40	—
B	7.20	9.50
C	1.20	1.30
D	4.80	5.30
All Dimensions in mm		

Mechanical Data

- Case: DO-201AD, Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 1.2 grams (approx.)
- Mounting Position: Any
- Marking: Type Number

RoHS COMPLIANT

Maximum Ratings and Electrical Characteristics T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	SR5150	SR5200	Unit
Maximum repetitive peak reverse voltage	V_{RRM}	150	200	Volts
Maximum RMS voltage	V_{RMS}	105	150	Volts
Maximum DC blocking voltage	V_{DC}	150	200	Volts
Maximum average forward rectified current at T_L(see fig.1)	$I_{(AV)}$	5.0		Amps
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	150.0		Amps
Maximum instantaneous forward voltage at 5.0A	V_F	0.85	0.95	Volts
Maximum DC reverse current T_A=25℃	I_R	0.2		mA
at rated DC blocking voltage T_A=100℃		2.0		
Typical junction capacitance (NOTE 1)	C_J	200		pF
Typical thermal resistance (NOTE 2)	$R_{\theta JA}$	50.0		°C/W
Operating junction temperature range	$T_{J,}$	-50 to +150		°C
Storage temperature range	T_{STG}	-50 to +150		°C

Note: 1.Measured at 1MHz and applied reverse voltage of 4.0V D.C.

2.P.C.B. mounted with 0.2x0.2"(5.0x5.0mm) copper pad areas

RATINGS AND CHARACTERISTIC CURVES SR5150 THRU SR5200

FIG. 1- FORWARD CURRENT DERATING CURVE

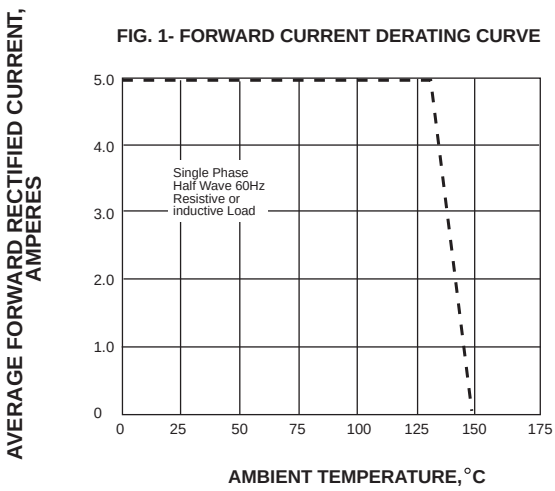


FIG. 2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

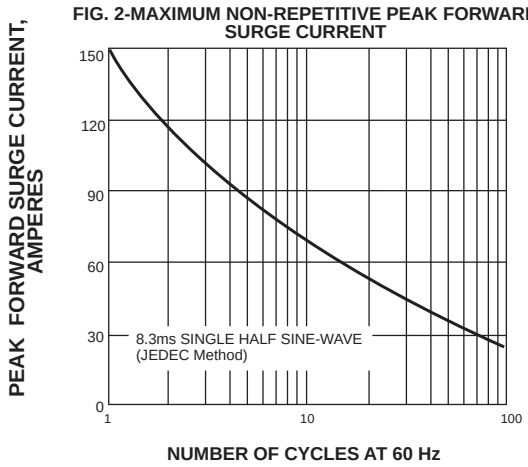


FIG. 3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

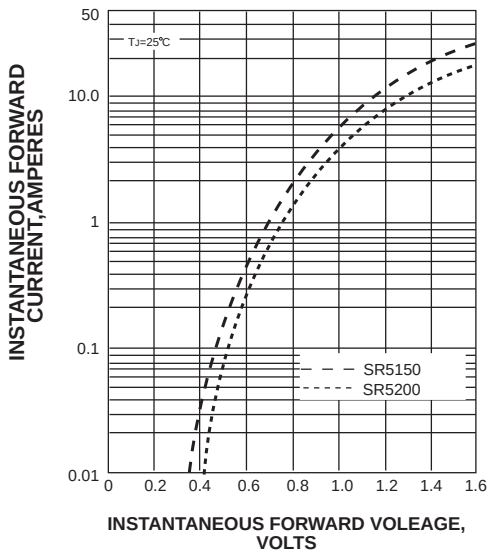


FIG. 4-TYPICAL REVERSE CHARACTERISTICS

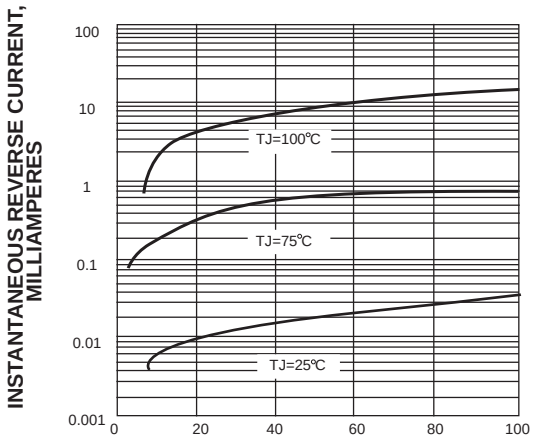


FIG. 5-TYPICAL JUNCTION CAPACITANCE

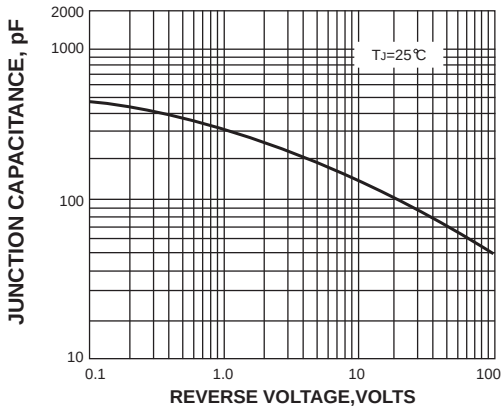


FIG. 6-TYPICAL TRANSIENT THERMAL IMPEDANCE

